

Description

The CSH05W2U3 is a high power TVS, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive lines. It is assembled into a 3-pin DFN2020-3 lead-free package. Each device will protect one line. The combination of small size, and high surge capability makes them ideal for use in applications such as cellular phones, LCD displays, MSB, and multi-media card



Features

- ◆ 5000 Watts peak pulse power (tp=8/20μs)
- ◆ Low leakage current
- ◆ Low clamping voltage
- ◆ Solid-state silicon-avalanche technology
- ◆ RoHS compliant

Mechanical Data

- ◆ Power lines
- ◆ Personal digital assistants (PDA's)
- ◆ Microprocessors based equipment
- ◆ Notebooks, desktops, and servers
- ◆ Cell phone handsets and accessories
- ◆ Portable electronics
- ◆ Peripherals

Ordering Information

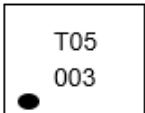
Part Number	Package	Marking	Material	Packing	Quantity per reel	Flammability Rating	Reel Size
CSH05W2U3	DFN2*2- 3L		Halogen free	Tape & Reel	3,000 PCS	UL 94V-0	7 inches

Table-1 Ordering information

Pin Configuration and Functions

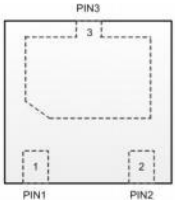

Outline	Circuit Diagram
	

Table-2 Pin configuration

Absolute Maximum Rating

Over operating free-air temperature range (unless otherwise noted)

Parameters	Symbol	Min.	Max.	Unit
Peak pulse power (tp=8/20μs)@25°C	P _{pk}	-	5000	W
Peak pulse current (tp=8/20μs)@25°C	I _{pp}	-	290	A
ESD (IEC61000-4-2 air discharge) @25°C	V _{ESD}	-	±30	kV
ESD (IEC61000-4-2 contact discharge) @25°C	V _{ESD}	-	±30	kV
Junction temperature	T _J	-	125	°C
Operating temperature	T _{OP}	-55	125	°C
Storage temperature	T _{STG}	-55	150	°C
Lead temperature	T _L	-	260	°C

Table-3 Absolute Maximum rating

Electrical Characteristics

At Ta = 25°C unless otherwise noted

Parameters	Symbol	conditions	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				5	V
Reverse Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	6	7		V
Reverse Leakage Current	I_R	$V_{RWM} = 5\text{V}$			5	μA
Clamping Voltage	V_{CL}	$I_{PP} = 50\text{A} ; TP = 8/20\mu\text{s}$		10	14	V
		$I_{PP} = 100\text{A} ; TP = 8/20\mu\text{s}$		12	16	
		$I_{PP} = 290\text{A} ; TP = 8/20\mu\text{s}$		17	22	
Junction capacitance	C_j	$V_{RWM} = 0\text{V}, f = 1\text{MHz}$		2200	2600	pF

Table-4 Electrical Characteristics

Ratings and Characteristic Curves (TA =25°C unless otherwise noted)

Figure 1. ESD clamping

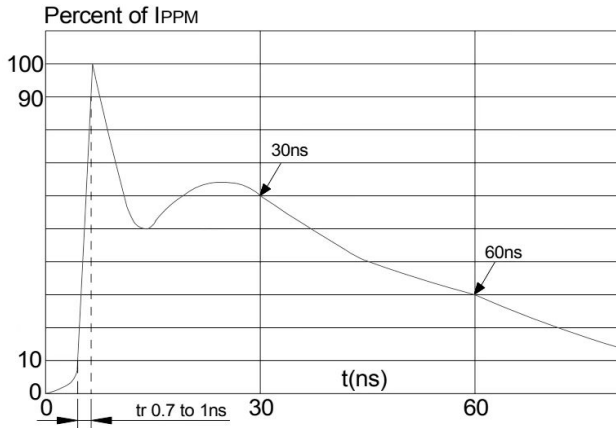


Figure 2. Peak pulse power derating curve
PPP derating in percentage(%)

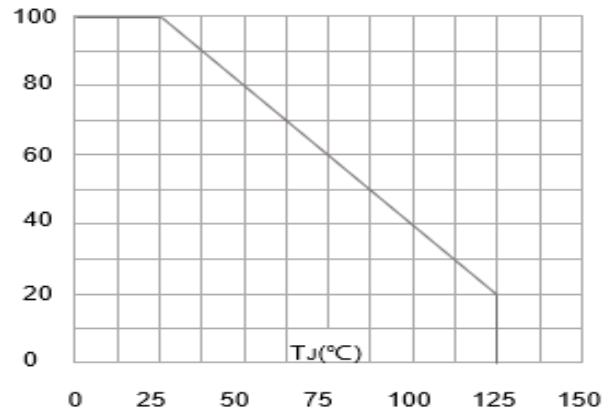


Figure 3. 8/20μs Pulse Waveform

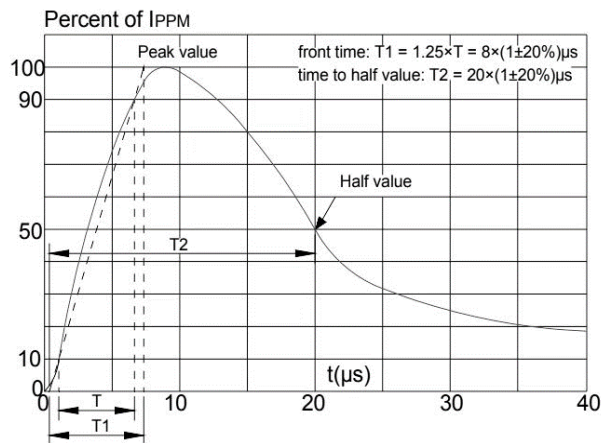
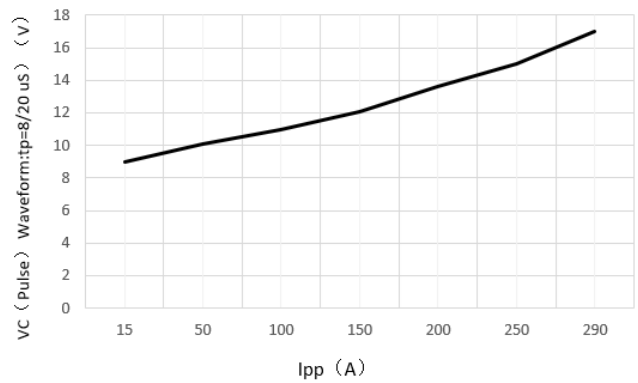
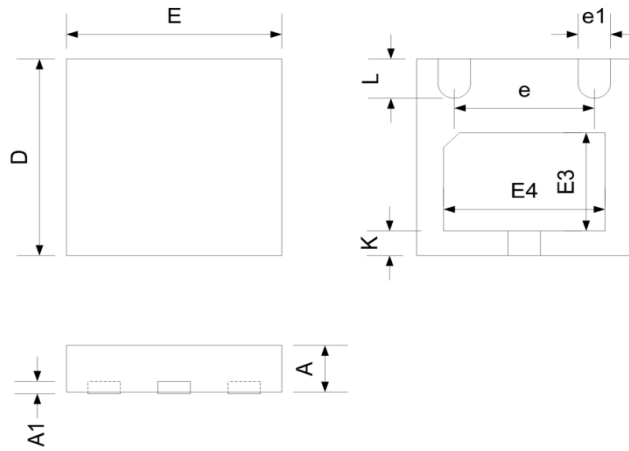


Figure 4. Clamping voltage vs.peak pulse current



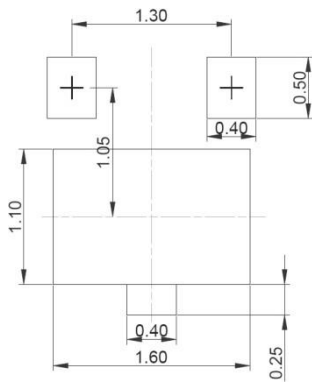
Dimension



Symbol	Dimensions in Millimeters		
	Min.	Typ.	Max.
D	1.900	2.000	2.100
E	1.900	2.000	2.100
e	1.200	1.300	1.400
e1	0.200	0.300	0.400
E3	0.850	1.000	1.150
E4	1,350	1.500	1.650
K	0.150	0.250	0.350
L	0.300	0,400	0.500
A	0.425	0.475	0.525
A1	0.100	0.125	0.150

Table-5 product dimension

Recommended Land Pattern



Note:

1. Controlling dimension: in millimeters
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference only

Revision history of Specification

Version	Change Items	Effective Date
1.0	Initial Release	20-Nov-2023